

INFORMATION DISCLOSURE STATEMENT

Case Name.	J.A. Garceran 2-12-20
Serial No.	09/784846
Applicant:	J.A. Garceran, et al.
Filing Date:	February 16, 2001
Group:	2681

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
	AG						
	AH						

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

JP	AI	Analog Devices, "High Speed Design Techniques, 9/96, pp. 6-25 to 6-30

***References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26.

*****Note First Page ONLY Header/Footer. Subsequent pages must ONLY have page # layout as header**

EXAMINER <i>f m h</i>	DATE CONSIDERED <i>3/20/04</i>
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U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
JP	AA	3,987,280 A	10/19/76	Bauer	235/ 150	G06J 1/00	5/21/75

JUN 27 2002

Technology Center 2600

FOREIGN PATENT DOCUMENTS


		Document Number	Date	Country	Class	Subclass	Translation
JP	AB	WO 99 07093 A	2/11/99	PCT	H03D H04B	7/00 1/04	Yes
JP	AC	WO 99 67878 A	12/29/99	PCT	H03D	7/00	Yes
JP	AD	EP 0 817 369 A2	1/7/98	EPC	H03D	7/00	Yes
JP	AE	EP 0 534 255 A2	3/31/93	EPC	H04L	5/06	Yes

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

JP	AF	European Search Report dated May 13, 2002.
	...	
	...	

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EXAMINER 	DATE CONSIDERED 3/20/04
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant